

**Amendments to the Specification:**

Please replace paragraph [0001] with the following amended paragraph:

[0001] This application is related to Application Serial Number 10/039,579, entitled “A Method and Apparatus for Suspending Execution of a Thread Until a Specified Memory Access Occurs”; Application Serial Number 10,039,656, entitled “Coherency Techniques for Suspending Execution of a Thread Until a Specified Memory Access Occurs”; Application Serial Number 10/039,650, entitled “Instruction Sequences for Suspending Execution of a Thread Until a Specified Memory Access Occurs” all filed on the same date as the present application and application serial number 09/489,130, entitled “Method and Apparatus for Pausing Execution in a Processor or the like”, now U.S. Patent 6,671,795 issued on December 30, 2003.

Please replace paragraph [0027] with the following amended paragraph:

[0027] Thus, the embodiments of Figures 1 and 2 provide techniques to allow a thread to be suspended by a program for a particular duration. In one embodiment, other events also cause T1 to be resumed. For example, an interrupt may cause T1 to resume. Figure 3b illustrates a flow diagram for one embodiment that allows other events to cause the suspend state to be exited. In block 360, the thread is already suspended according to previous operations. In block 365, whether sufficient time has elapsed (as previously discussed with respect to Figure 2) is tested. In the event that sufficient time has elapsed, then thread T1 is resumed, as indicated in block 380.

Please replace the paragraph following paragraph [0031] on page 13 with the following amended paragraph:

Figure 5 illustrates various design representations or formats for simulation, emulation, and fabrication of a design using the disclosed techniques. Data representing a design may represent the design in a number of manners. First, as is useful in simulations, the hardware may be represented using a hardware description language or another functional description language which essentially provides a computerized model of how the designed hardware is expected to perform. The hardware model 510 may be stored in a storage medium 500 such as a computer memory so that the model may be simulated using simulation software 520 that applies a particular test suite 530 to the hardware model 510 to determine if it indeed functions as intended. In some embodiments, the simulation software is not recorded, captured, or contained in the medium.

Please replace the paragraph-preceding paragraph [0032] on page 14 with the following amended paragraph:

In any representation of the design, the data may be stored in any form of a computer readable medium. An optical or electrical wave 560 modulated or otherwise generated to transmit such information, a memory 550, or a magnetic or optical storage 540 such as a disc may be the medium. The set of bits describing the design or the particular part of the design are an article that may be sold in and of itself or used by others for further design or fabrication.